

# MAROC Digital Output

www.maroc-digital-output.com

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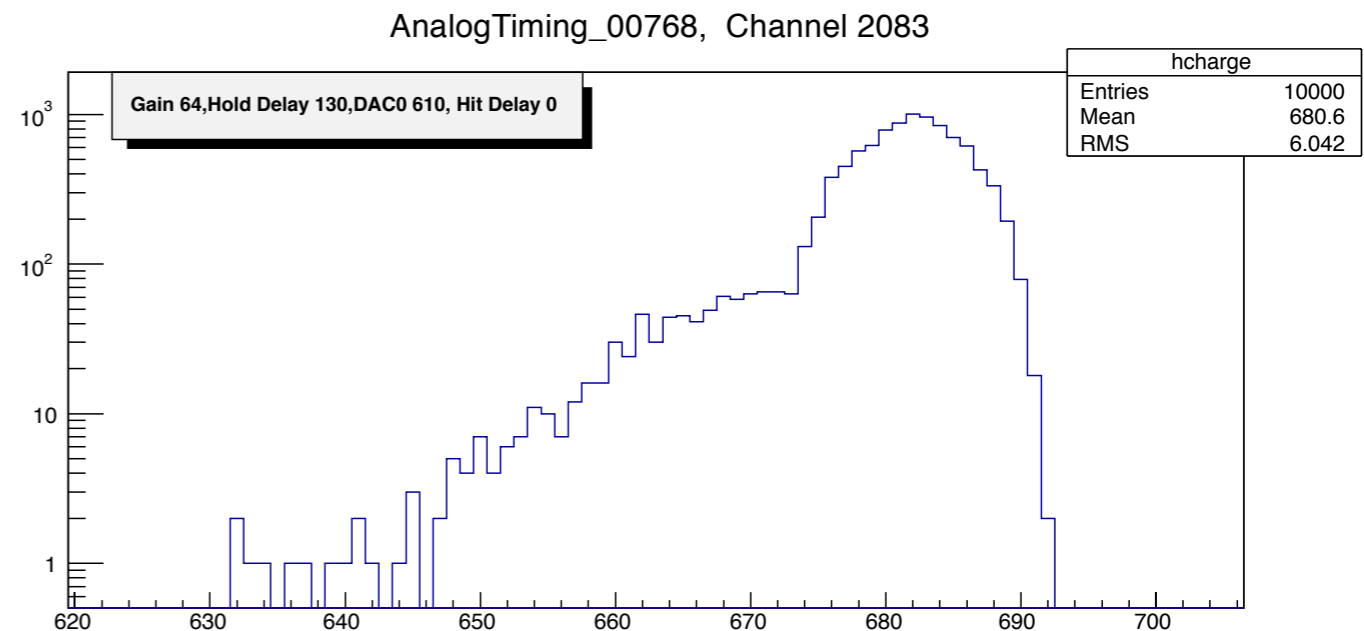
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CLAS12 RICH R&D, 2013 May 17th, ISS

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# Intro

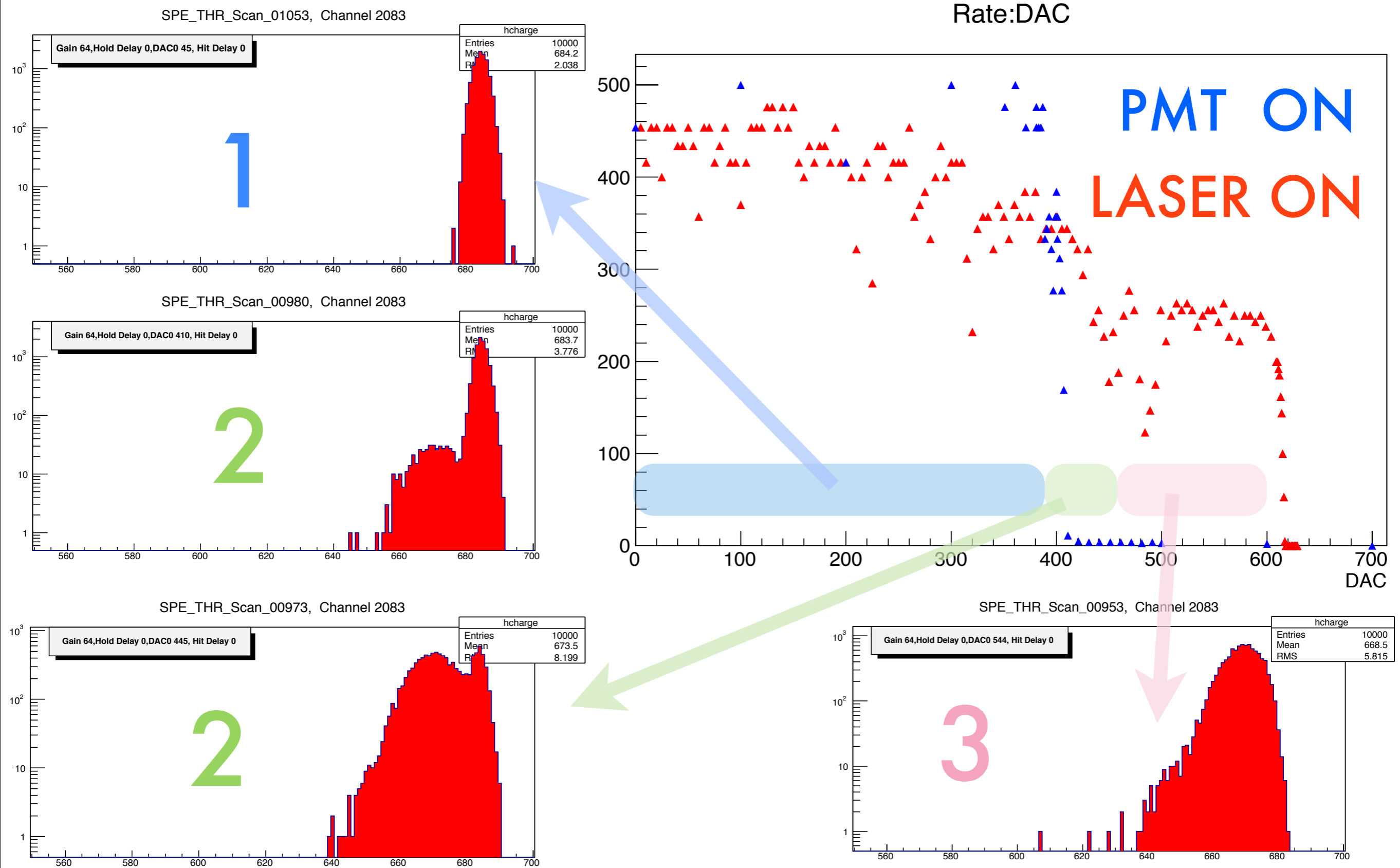
- Light Source at spe level



Light level characterization in External Trigger

- THR scan (DAC) with Internal Trigger
  - ★ Rate vs DAC
  - ★ ADC Spectrum vs DAC

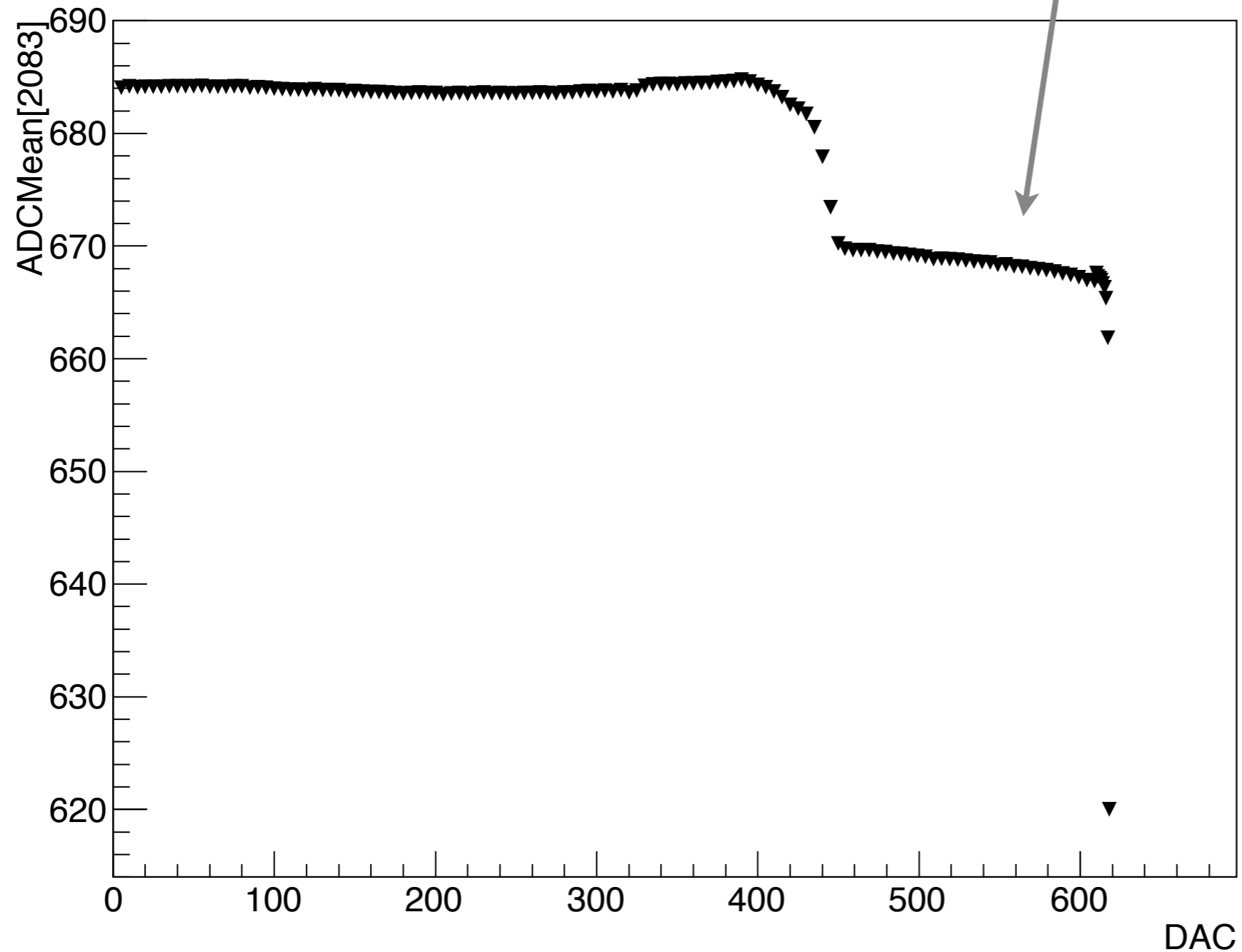
# Self Trigger - THR Scan (dac)



# in other words

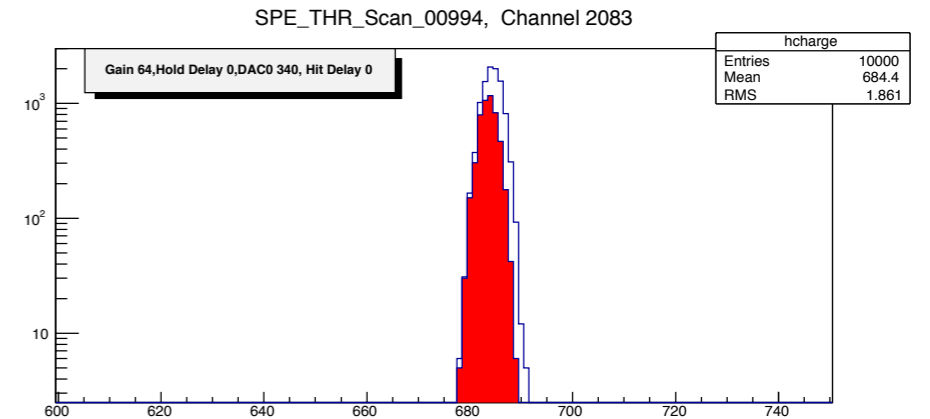
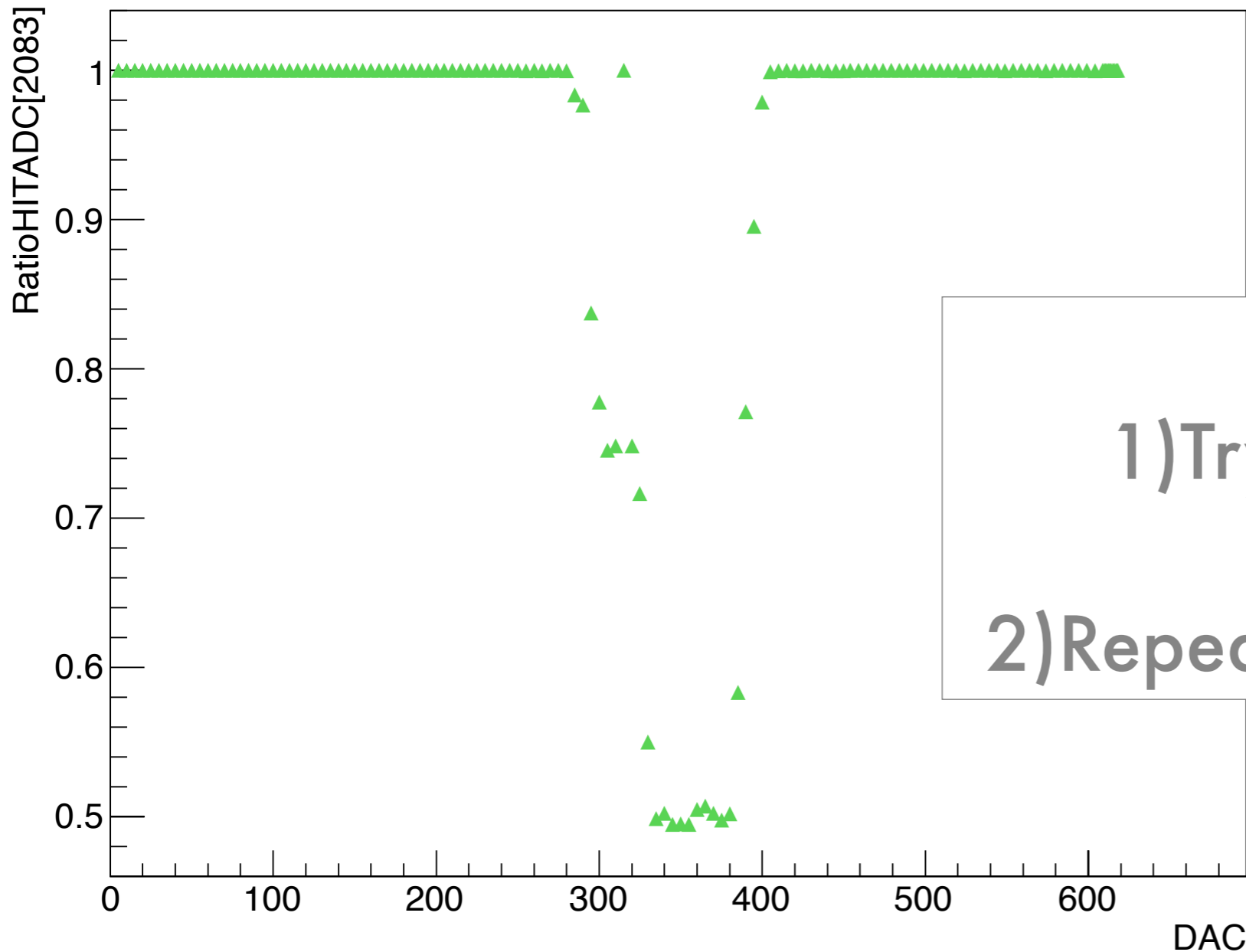
Only signal, NO pedestal

ADCMean[2083]:DAC



# bad synchronization

RatioHITADC[2083]:DAC



Next steps:  
1) Try different (relative) delay  
2) Repeat in External Trigger